



Title: SEMICONDUCTOR MEMORY DEVICE BASED ON
DUMMY-CELL METHOD
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TP
7/13/04

FIG.2

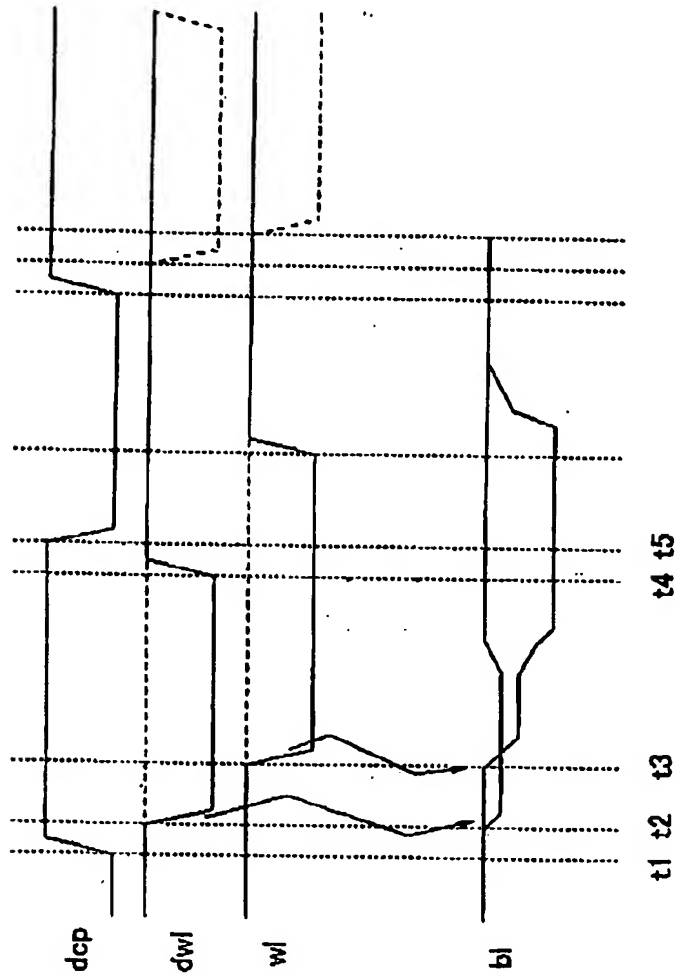


FIG.3

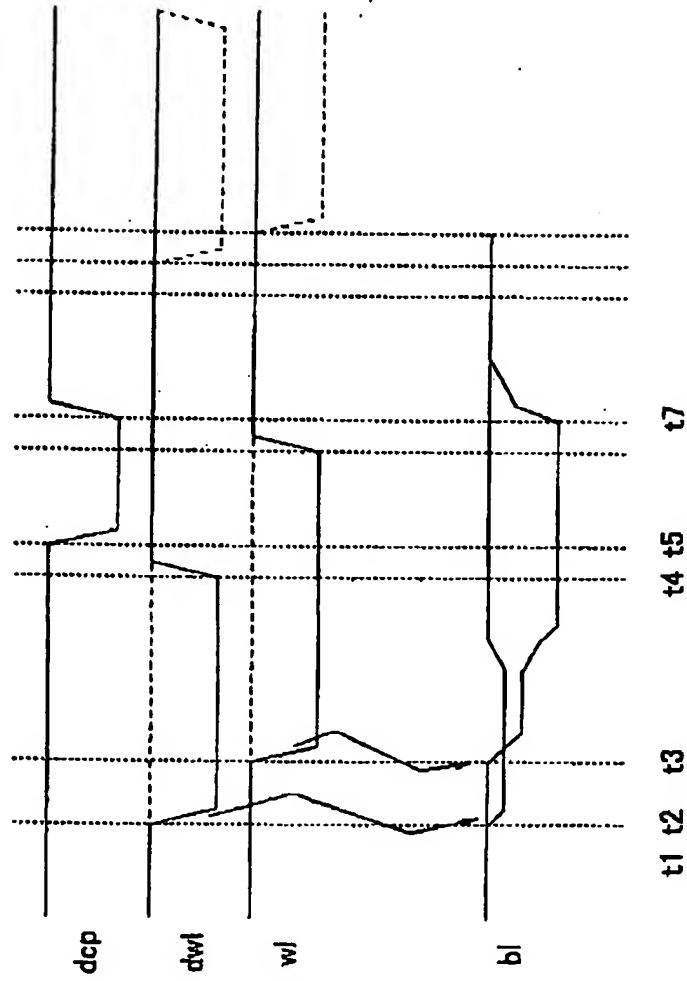


FIG. 4

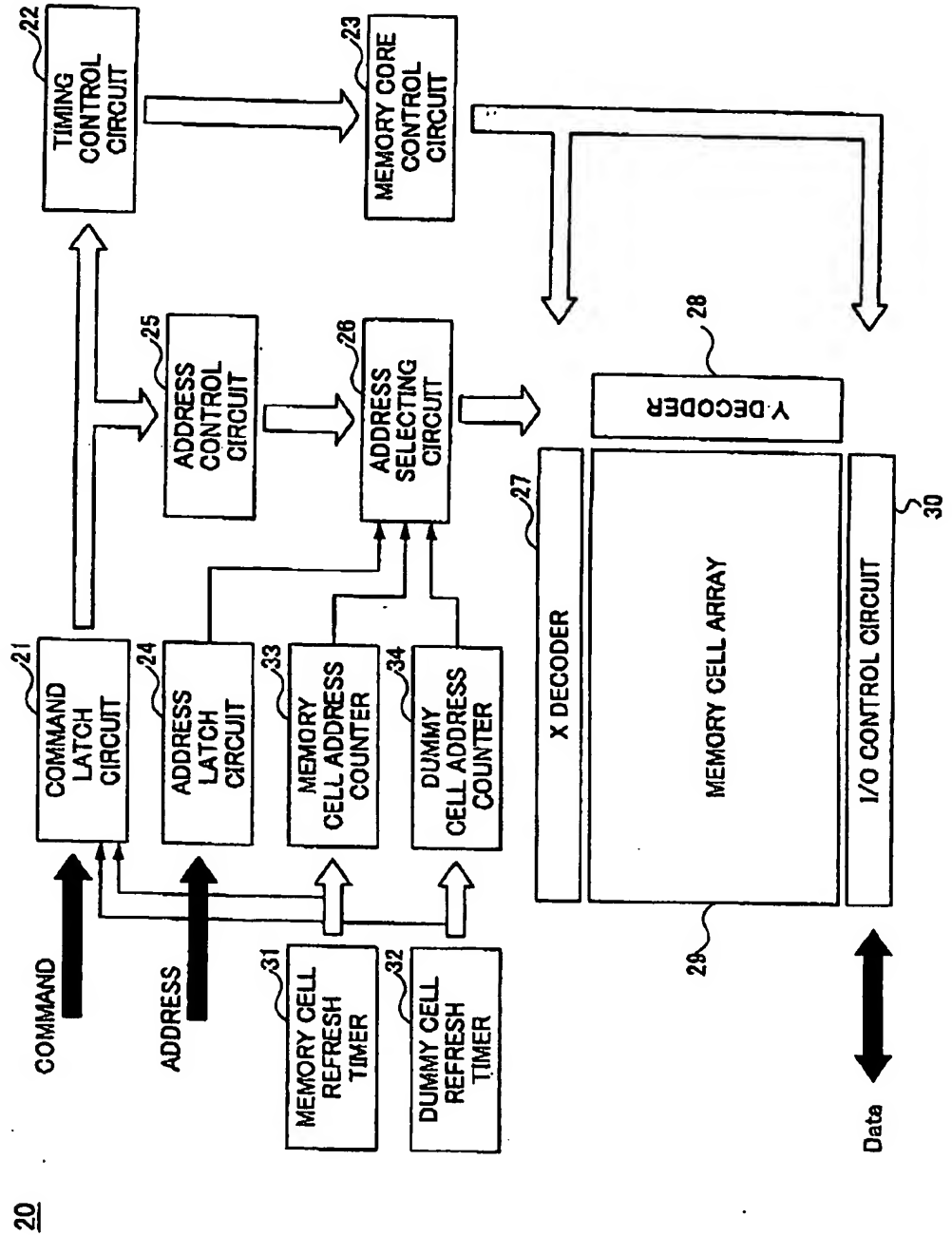


FIG. 6

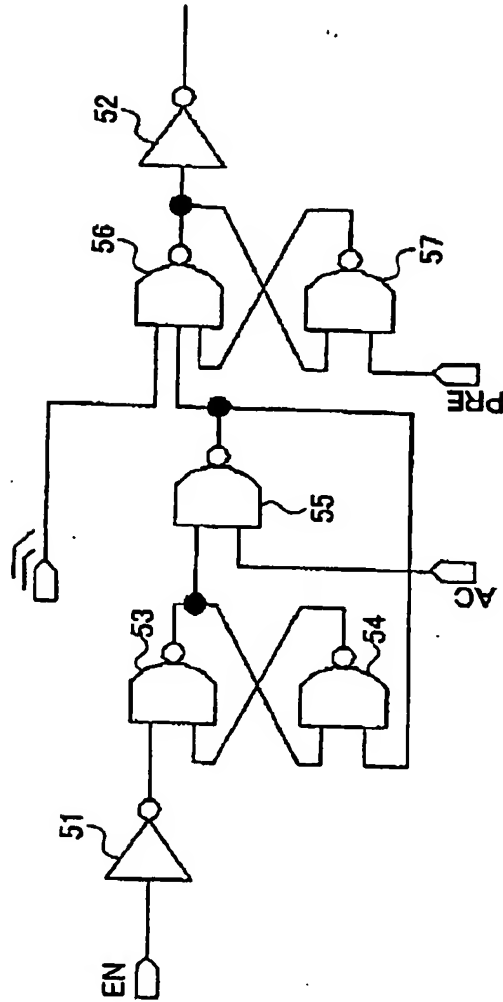


FIG.10

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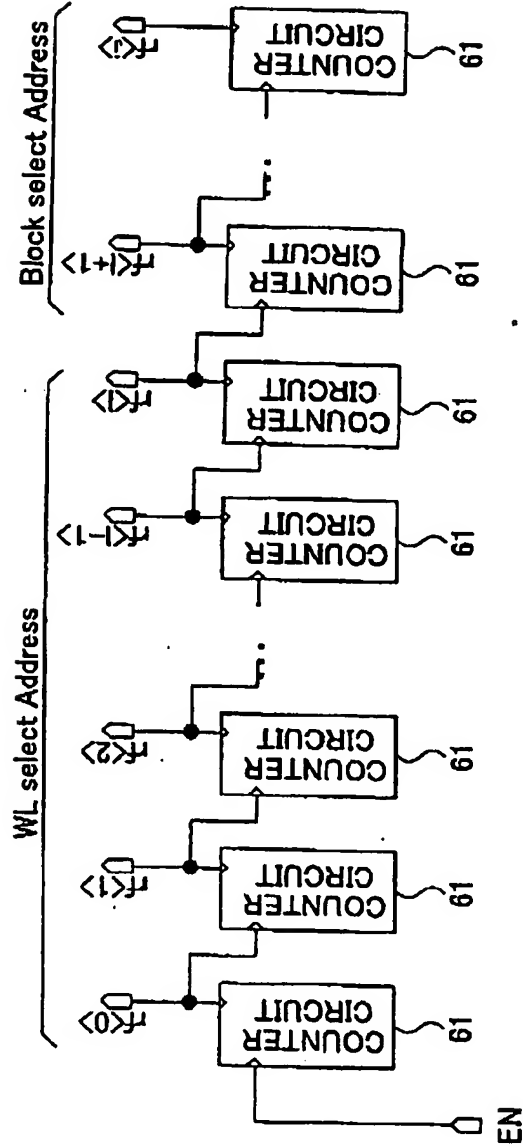


FIG.11

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